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HIT 2 010-1-1

first program or the data to the ROM under control of the central processing unit;

a memory in which a second program as a write control program for writing to the ROM is stored; and

an input and output unit;

wherein the central processing unit performs a writing to the ROM of the first program input from outside of the semiconductor chip via the input and output unit by controlling the write control circuit based on the second program,

wherein the first program includes an instruction which changes a process of the central processing unit to a process that controls a writing of the ROM based on the second program stored in the memory,

wherein the second program includes an instruction which returns the process of the CPU to a process based on the first program stored in the ROM after completion of the process that controls the writing of the ROM, and

wherein significant amounts of the first program stored in the ROM are written in a write process where the central processing unit controls the write control circuit by executing the second program.